Attorney Docket No. 1614.1082

ATTENTION

**BRANCH** 

**CERTIFICATE OF CORRECTION** 

## STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hideo MIYAKE et al.

Application No.: 09/671,117

Confirmation No.: 8617

Filed: September 28, 2000

U.S. Patent No.: 6,868,472 6

Issued: March 15, 2005

METHOD OF CONTROLLING A CACHE MEMORY TO INCREASE AN ACCESS

SPEED TO A MAIN MEMORY, AND A COMPUTER USING THE METHOD

## REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

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of Correction

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,868,472 B)

**DATED**: March 15, 2005

INVENTOR(S): Hideo MIYAKE et al.

Cover Page, (56) References Cited, FOREIGN PATENT DOCUMENTS, please change "EP 0 568 221 4/1993" to -- EP 0 568 221 11/1993 --

Column 42, line 48, after "claim 4" insert - - , - -

Column 42, line 59, after "claim 6, insert - - , - -

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PATENT NO. 6,868,472

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